

ACKNOWLEDGEMENT RECEIPT

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Title of Invention	METHOD FOR DESIGNING AN INTEGRATED CIRCUIT DEFECT MONITOR																																																																																																														
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/710940 </p> <p>EFS ID: 66574</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 30%;">Confirmation Code</th> <th style="width: 70%;">Message</th> </tr> </thead> <tbody> <tr> <td style="padding: 5px;">ISVR1</td> <td style="padding: 5px;">Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td style="padding: 5px;">ICON1</td> <td style="padding: 5px;">4939</td> </tr> <tr> <td style="padding: 5px;">USPTOEFSNot</td> <td style="padding: 5px;">For assistance with e-filing a patent application, contact the Patent Electronic Business Center: Toll-Free Number: 1(866) 217-9197 Website: http://www.uspto.gov/ebc/</td> </tr> </tbody> </table> <p>First Named Applicant: John Cohn</p> <p>Attorney Docket Number: BUR920040164US1</p> <p>Timestamp: 2004-08-13 13:20:00 EDT</p> <p>From: US</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: small;"> <thead> <tr> <th style="width: 30%;">Doc. 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